

ANALOG MULTIPLEXERS WITH CMOS CONTROL SIGNALS

ABSTRACT

- 5 Analog multiplexer circuits with CMOS control signals and with low signal feed-through and high bandwidth are described. These circuits emphasize low parasitic capacitance through circuit layout techniques and the use of smaller size n-channel transistors where possible.
- 10 These circuits can be used for both single-ended and differential configurations. Two embodiments of the circuit are discussed allowing for optimal selection of multiplexers in application requirements ranging from lower-to-higher bandwidth and small-to-large input signal
- 15 size.